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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei YAMAZAKI et al.      Art Unit : 2871  
Serial No. : 09/809,646      Examiner : Unknown  
Filed : March 16, 2001  
Title : SEMICONDUCTOR DISPLAY DEVICE AND MANUFACTURING METHOD  
THEREOF

Commissioner for Patents  
Washington, D.C. 20231

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Supplemental to an information disclosure statement filed on June 13, 2002, Applicants submit the English translations for the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 11, 2002

*John F. Hayden*  
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John F. Hayden  
Reg. No. 37,640

Fish & Richardson P.C.  
1425 K Street, N.W.  
11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331

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